Harmonic Mitigation using 36-Pulse AC-DC Converter for Direct Torque Controlled Induction Motor Drives

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ABSTRACT
This paper presents the design and analysis of a transformer based 36-pulse ac-dc converters which supplies direct torque controlled induction motor drives (DTCIMD’s) in order to have better power quality conditions at the point of common coupling. The converters output voltage is accomplished via two paralleled eighteen-pulse ac-dc converters each of them consisting of nine-phase diode bridge rectifier. The design procedure of magnetics is in a way such that makes it suitable for retrofit applications where a six-pulse diode bridge rectifier is being utilized. The 36-pulse structure improves power quality criteria at ac mains and makes them consistent with the IEEE-519 standard requirements for varying loads. Furthermore, near unity power factor is obtained for a wide range of DTCIMD operation. A comparison is made between 6-pulse and 36-pulse converters (Polygon, Fork, and Hexagon) from view point of power quality indices. Results show that input current total harmonic distortion (THD) is less than 4% for the 36-pulse topologies at variable loads. The Delta/Hexagon connected platform could simplify the resulted configuration for the converters and reducing the costs.

Keywords: AC–DC converter, power quality, 36-pulse rectifier, direct torque controlled induction motor drive (DTCIMD).

1. Introduction
Recent advances in solid state conversion technology has led to the proliferation of variable frequency induction motor drives (VFIMD’s) that are used in several applications such as air conditioning, blowers, fans, pumps for waste water treatment plants, textile mills, rolling mills etc [1]. The most practical technique in VFIMD’s is direct torque controlled strategy in that it offers better performance rather than the other control techniques. Direct Torque controlled technique is implemented in voltage source inverter which is mostly fed from six-pulse diode bridge rectifier, Insulated gate bipolar transistors (IGBT’s) are employed as the VSI switches. The most important drawback of the six-pulse diode-bridge rectifier is its poor power factor injection of current harmonics into ac mains. The circulation of current harmonics into the source impedance yields in harmonic polluted voltages at the point of common coupling (PCC) and consequently resulting in undesired supply voltage conditions for costumers in the vicinity. The value of current harmonic components which are injected into the grid by nonlinear loads such as DTCIMD’s should be confined within the standard limitations. The most prominent standards in this field are IEEE standard 519 [2] and the International Electrotechnical Commission (IEC) 61000-3-2 [3].

According to considerable growth of Static Power Converters (SPC’s) that are the major sources of harmonic distortion and as a result their power quality problems, researchers have focused their attention on harmonic eliminating solutions. For DTCIMD’s one effective solution is to employ multipulse AC-DC converters. These converters are based on either phase multiplication or phase shifting or pulse doubling or a combination [4]-[19]. Although, in the conditions of light load or small source impedance, line current total harmonic distortion (THD) will be more than 5% for up to 18-pulse AC-DC converters. A Hexagon-Connected Autotransformer-Based 24-pulse AC-DC converter is reported in [7] which has THD variation of 4.48% to 5.65% from full-load to light-load (20% of full-load). A Zigzag-Connected Autotransformer-Based 24-pulse AC-DC converter is reported in [13] which has THD variation of 4.51% to 5.77% from full-load to light-load (20% of full-load).
Another T-Connected Autotransformer-Based 24-Pulse AC–DC Converter has also been presented in [14], however, the THD of the supply current with this topology is reported to vary from 2.46% to 5.20% which is more than 5% when operating at light load. The 36-pulse one was designed for vector controlled induction motor drives in [17] which has THD variation of 2.03% to 3.74% from full-load to light-load (20% of full-load) respectively but the dc link voltage is higher than that of a 6-pulse diode bridge rectifier, thus making the scheme nonapplicable for retrofit applications.

The delta/polygon-connected transformer-based 36-pulse ac-dc converter (shown in Fig. 1) for power quality improvement in [18] which has THD variation of 2.92% to 3.89% from full-load to light-load (20% of full-load) respectively and Delta/Fork-Connected Transformer-based 36-pulse ac–dc converter (shown in Fig. 2) have been reported [19] for reducing the total harmonic distortion (THD) of the ac mains current. But these topologies require higher rating magnetics, resulting in the enhancement of capital cost. As is mentioned before, the Delta/Hexagon connected platform could simplify the resulted configuration for the converters and reducing the costs (shown in Fig. 3). The Delta/Hexagon scheme has an optimized configuration in this regard. The proposed design method will be suitable even when the transformer output voltages vary while keeping its 36-pulse operation. In the 36-pulse structure, two nine-leg diode-bridge rectifiers are paralleled via two interphase transformers (IPTs) and fed from a transformer. Hence, a 36-pulse output voltage is obtained. Detailed design tips of the IPT and totally the whole structure of 36-pulse ac-dc converter are described in this paper and the proposed converter is modeled and simulated in MATLAB to study its behavior and specifically to analyze the power quality indices at ac mains. Furthermore, a 36-pulse ac-dc converter consisting of a delta/hexagon transformer, two eighteen-pulse diode bridge rectifiers paralleled through two IPTs, and with a DTCIMD load Fig. 3.

Figure 1. Transformer configuration, Winding arrangement, and Phasor representation of transformer for 36-pulse AC-DC converter having delta/Polygon connected secondary winding [18].
Simulation results of six-pulse and proposed 36-pulse ac-dc converters feeding a DTCIMD load are scheduled and various quality criteria such as THD of ac mains current, power factor, displacement factor, distortion factor, and THD of the supply voltage at PCC are compared.

2. Proposed 36-Pulse AC–DC Converter

In order to implement a 36-pulse ac-dc converter through paralleling two bridge rectifiers, i.e. two 18-pulse rectifiers, two sets of nine-phase voltages with a phase difference of 40 degrees between the voltages of each group and 10 degrees between the same voltages of the two groups are required.

Accordingly, each bridge rectifier consists of nine common-anode and nine common-cathode diodes (two nine-leg rectifiers). Phasor diagram of delta/hexagon transformer is shown in Fig. 4.

2.1 Design of Proposed Transformer for 36-Pulse AC–DC Converter

The hexagon transformer winding arrangement for 36-pulse AC-DC conversion is shown in Fig. 5 and its connection along with phasor diagram. The aforementioned two voltage sets are called as \((V_{a1}, V_{a2}, V_{a3}, V_{a4}, V_{a5}, V_{a6}, V_{a7}, V_{a8}, V_{a9})\) and \((V_{b1}, V_{b2}, V_{b3}, V_{b4}, V_{b5}, V_{b6}, V_{b7}, V_{b8}, V_{b9})\) that are fed to rectifiers I and II,
respectively. The same voltages of the two groups, i.e. Vai and Vbi, are phase displaced of 10 degrees. V¬a1 and Vb1 has a phase shift of +5 and -5 degrees from the input voltage of phase A, respectively. According to phasor diagram, the nine-phase voltages are made from ac main phase and line voltages with fractions of the primary winding turns which are expressed with the following relationships. Consider three-phase voltages of primary windings as follows:

$$\begin{align*}
V_A & = V_a < 0^\circ, V_B = V_c < -120^\circ, V_C = V_e < 120^\circ. \\
\text{Where, nine-phase voltages are:} \\
V_{a1} & = V_a < 5^\circ, V_{a2} = V_a < -35^\circ, V_{a3} = V_a < -75^\circ, \\
V_{a4} & = V_a < -115^\circ, V_{a5} = V_a < -155^\circ, V_{a6} = V_a < -195^\circ, \\
V_{a7} & = V_a < -235^\circ, V_{a8} = V_a < -275^\circ, V_{a9} = V_a < -315^\circ. \\
V_{b1} & = V_b < 5^\circ, V_{b2} = V_b < -45^\circ, V_{b3} = V_b < -85^\circ, \\
V_{b4} & = V_b < -125^\circ, V_{b5} = V_b < -165^\circ, V_{b6} = V_b < -205^\circ, \\
V_{b7} & = V_b < -245^\circ, V_{b8} = V_b < -285^\circ, V_{b9} = V_b < -325^\circ. \\
\end{align*}$$

Input voltages for converter I are:

$$\begin{align*}
V_{a1} & = V_a + K_1 V_C - K_2 V_B \\
V_{a2} & = V_a + K_2 V_B - K_1 V_C \\
V_{a3} & = V_a + K_3 V_A - K_4 V_C \\
V_{a4} & = V_B + K_1 V_C - K_2 V_A \\
V_{a5} & = V_B + K_3 V_B - K_4 V_A \\
V_{a6} & = V_C + K_1 V_A - K_2 V_B \\
V_{a7} & = V_C + K_3 V_C - K_4 V_B \\
V_{a8} & = V_A + K_1 V_B - K_4 V_C \\
V_{a9} & = V_A + K_2 V_C - K_3 V_B \\
\end{align*}$$

Input voltages for converter II are:

$$\begin{align*}
V_{a1} & = V_A + K_1 V_B - K_2 V_C \\
V_{a2} & = V_A + K_2 V_C - K_1 V_B \\
V_{a3} & = V_A + K_3 V_A - K_4 V_B \\
V_{a4} & = V_B + K_1 V_B - K_2 V_A \\
V_{a5} & = V_B + K_3 V_C - K_4 V_A \\
V_{a6} & = V_C + K_1 V_A - K_2 V_B \\
V_{a7} & = V_C + K_3 V_C - K_4 V_B \\
V_{a8} & = V_A + K_1 V_B - K_4 V_C \\
V_{a9} & = V_A + K_2 V_C - K_3 V_B \\
\end{align*}$$

$$\begin{align*}
V_{AB} & = \sqrt{3} V_A < 30^\circ, V_{BC} = \sqrt{3} V_B < 30^\circ, V_{CA} = \sqrt{3} V_C < 30^\circ. \\
\end{align*}$$

Figure 4. Phasor representation of transformer for 36-pulse AC-DC converter having hexagon connected secondary winding.

Figure 5. Winding arrangement of transformer for 36-pulse AC-DC converter having hexagon connected secondary winding.
Constants $K_1$-$K_6$ are calculated using (2)-(6) to obtain the required windings turn numbers to have the desired phase shift for the two voltage sets:

$$K_1 = 0.05411, K_2 = 0.04651, K_3 = 0.5120,$$
$$K_4 = 0.1503, K_5 = 0.7011, K_6 = 0.1153.$$  \hspace{1cm} (7)

### 2.2 Design of Transformer for Retrofit Applications

The value of output voltage in multipulse rectifiers boosts relative to the output voltage of a six-pulse converter making the multipulse rectifier inappropriate for retrofit applications. For instance, with the transformer arrangement of the proposed 36-pulse converter, the rectified output voltage is 17% higher than that of six-pulse rectifier. For retrofit applications, the above design procedure is modified so that the dc-link voltage becomes equal to that of six-pulse rectifier. This will be accomplished via modifications in the tapping positions on the windings as shown in Fig. 6. It should be noted that with this approach, the desired phase shift is still unchanged. Similar to section II part 1, the following equations can be derived as:

$$|V_3| = 0.8314|V_A|.$$  \hspace{1cm} (8)

Input voltages for converter I are:

$$V_{a1} = V_A + K_1 V_C + K_2 V_B$$
$$V_{a2} = V_A + K_1 V_B + K_2 V_C$$
$$V_{a3} = V_A - K_3 V_A + K_6 V_C$$
$$V_{a4} = V_B + K_5 V_A + K_2 V_C$$
$$V_{a5} = V_B + K_5 V_C + K_4 V_A$$
$$V_{a6} = V_C + K_5 V_B + K_6 V_A$$
$$V_{a7} = V_C + K_1 V_B + K_2 V_A$$
$$V_{a8} = V_C + K_1 V_C + K_4 V_B$$
$$V_{a9} = V_A + K_3 V_C + K_6 V_B$$  \hspace{1cm} (9)

Accordingly, the values of constants $K_1$-$K_6$ are changed for retrofit applications as:

$$K_1 = 0.2136, K_2 = 0.12994, K_3 = 0.59428,$$
$$K_4 = 0.04364, K_5 = 0.75154, K_6 = 0.0727.$$  \hspace{1cm} (11)

The values of $K_1$-$K_6$ establish the essential turn numbers of the transformer windings to have the required output voltages and phase shifts. To ensure the independent operation of the rectifier groups, interphase transformers (IPTs), which are relatively small in size, are connected at the output of the rectifier bridges. With this arrangement, the rectifier diodes conduct for 120 per cycle. The kilovoltampere rating of the transformer is calculated as [4]:

$$kVA = 0.5 \sum V_{\text{winding}} I_{\text{winding}}.$$  \hspace{1cm} (12)

Where, $V_{\text{winding}}$ is the voltage across each transformer winding and $I_{\text{winding}}$ indicates the full load current of the winding. The apparent power rating of the interphase transformer is also calculated in a same way. Another important parameter related to the AC-DC converters is transformer utilization factor (TUF) that indicates the relative size of transformers is defined as:
kVA = P_{DC} / \Sigma V_{sec} I_{sec} \tag{13}

Where $V_{sec}$ and $I_{sec}$ is rms voltage and current rating of secondary winding.

3. Matlab Simulation

The designed configurations were simulated using SIMULINK and power system block set (PSB) toolboxes. In this model, a three-phase 460 V and 60 Hz network is utilized as the supply for the 36-pulse converter. The designed transformer is modeled via three multi-winding transformers. Multi-winding transformer block is also used to model IPT. At the converter output, a series inductance ($L$) and a parallel capacitor ($C$) as the dc link are connected to IGBT-based Voltage Source Inverter (VSI). VSI drives a squirrel cage induction motor employing direct torque control strategy. The simulated motor is 50 hp (37.3 kW), 4-pole, and Y-connected. Detailed data of motor are listed in Appendix. Simulation results are depicted in Figs. 7-17. Power quality parameters are also listed in Table I for 6-pulse and 36-pulse ac-dc converters.

4. Results and Discussion

Table I lists the power quality indices obtained from the simulation results of the 6-pulse and 36-pulse converters. Fig. 7 depicts two groups of nine-phase voltage waveforms with a phase shift of 10 degrees between the same voltages of each group. The voltage across the interphase transformer (shown in Fig. 8) has a frequency equal to 9 times that of the supply which results in a significant reduction in volume and cost of magnetics. The 36-pulse converter output voltage (shown in Fig. 9) is almost smooth and free of ripples and its average value is 605.7 volts which is approximately equal to the DC link voltage of a six-pulse rectifier (607.6 volts). This makes the 36-pulse converter suitable for retrofit applications. Input current waveforms and its harmonic spectrum of the 6-pulse and 36-pulse converters extracted and shown in Figs. 10-17, respectively to check their consistency with the limitations of the IEEE standard 519. These harmonic spectrums are obtained when induction motor operates under light load (20% of full load) and full load conditions.
Obviously, for 6-pulse converter, fifth and seventh order harmonics are dominant. Hence, input current THD of this converter will be relatively a large amount and is equal to 28.53% and 52.53% for full load and light load conditions that are not within the standard margins. It is observed from the results that the THD of ac mains current at light load (20%) in Topology Polygon is 3.82, shown in Fig. 12, and that at full load is 2.92%, as shown in Fig. 13. Similarly, Fig. 14 shows the THD of ac mains current at light load in Topology Fork as 3.47% and at full load as 1.59%, shown in Fig. 15.

Table 1. Comparison of Simulated Power Quality Parameters of the DTCIMD Fed from Different AC-DC Converters.

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Topology</th>
<th>% THD of Vac</th>
<th>AC Mains Current ISA (A)</th>
<th>% THD of ISA, at</th>
<th>Distortion Factor, DF</th>
<th>Displacement Factor, DPF</th>
<th>Power Factor, PF</th>
<th>DC Voltage (V)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Light Load</td>
<td>Full Load</td>
<td>Light Load</td>
<td>Light Load</td>
<td>Light Load</td>
<td>Light Load</td>
<td>Light Load</td>
</tr>
<tr>
<td>1</td>
<td>6-pulse</td>
<td>5.64</td>
<td>10.33</td>
<td>52.69</td>
<td>52.53</td>
<td>28.53</td>
<td>0.8850</td>
<td>0.8730</td>
</tr>
<tr>
<td>2</td>
<td>36-pulse polygon</td>
<td>2.93</td>
<td>10.57</td>
<td>52.52</td>
<td>3.89</td>
<td>2.92</td>
<td>0.9992</td>
<td>0.9993</td>
</tr>
<tr>
<td>2</td>
<td>36-pulse Fork</td>
<td>2.16</td>
<td>10.47</td>
<td>52.43</td>
<td>3.65</td>
<td>1.59</td>
<td>0.9993</td>
<td>0.9995</td>
</tr>
<tr>
<td>2</td>
<td>36-pulse Hexagon</td>
<td>1.86</td>
<td>10.53</td>
<td>52.23</td>
<td>3.26</td>
<td>1.88</td>
<td>0.9995</td>
<td>0.9981</td>
</tr>
</tbody>
</table>
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Figure 13. Input current waveform of 36-pulse ac–dc converter at full load and its harmonic spectrum for Topology polygon. (50hp load).

On the other hand, as shown in Figs. 16-17, Hexagon connected 36-pulse converter has an acceptable current THD (3.26% for light load and 1.88% for full load conditions). In this configuration, low order harmonics up to 33rd are eliminated in the supply current. In general, the largely improved performance of the 36-pulse converter makes the power quality indices such as THD of supply current and voltage (THDi and THDv), displacement power factor (DPF), distortion factor (DF), and power factor (PF) satisfactory for different loading conditions.

Figure 14. Input current waveform of 36-pulse ac–dc converter at light load and its harmonic spectrum for Topology Fork. (50hp load).

Figure 15. Input current waveform of 36-pulse ac–dc converter at full load and its harmonic spectrum for Topology Fork. (50hp load).

Figure 16. Input current waveform of 36-pulse ac–dc converter at light load and its harmonic spectrum for Topology Hexagon. (50hp load).

Figure 17. Input current waveform of 36-pulse ac–dc converter at full load and its harmonic spectrum for Topology Hexagon. (50hp load).
The aforementioned criteria are listed in Table I for the three types of converters. The converter in Topology Polygon, Fork, and Hexagon needs magnetics of 124.84%, 132.54%, and 106.35% of the drive rating respectively. The Delta/Hexagon connected platform could simplify the resulted configuration for the converters and reducing the costs. It can be observed that the TUF for Delta/Hexagon connected 36-pulse AC-DC converter is 89.46% which is better than the Delta/Polygon and Delta/Fork connected transformers used for 36-pulse AC-DC converter.

5. Conclusion

This paper presents the design and analysis of a transformer based 36-pulse ac-dc converters which supplies direct torque controlled induction motor drives (DTCIMD’s) in order to have better power quality conditions at the point of common coupling. The 36-pulse converter output voltage is accomplished via two paralleled eighteen-pulse ac-dc converters each of them consisting of nine-phase diode bridge rectifier. Afterwards, the proposed design procedure was modified for retrofit applications. Simulation results prove that, for the 36-pulse topology, input current distortion factor is in a good agreement with IEEE 519 requirements. Current THD is less than 4% for varying loads. It was also observed that the input power factor is close to unity resulting in reduced input current for DTCIMD load. Thus, the 36-pulse ac–dc converter can easily replace the existing 6-pulse converter without much alteration in the existing system layout and equipment. The Delta/Hexagon connected platform could simplify the resulted configuration for the converters and reducing the costs. The Delta/Hexagon scheme has an optimized configuration in this regard. The Delta/Hexagon design method will be suitable even when the transformer output voltages vary while keeping its 36-pulse operation.
References


Appendix

Motor and Controller Specifications:

Three-phase squirrel cage induction motor—50 hp (37.3 kW), three phase, four pole, Y-connected, 460 V, 60 Hz. Rs = 0.0148 Ω; Rr = 0.0092 Ω; Xls = 1.14 Ω; Xlr = 1.14 Ω, XLm = 3.94 Ω, J = 3.1 Kg • m2.

Controller parameters: PI controller Kp = 300; Ki = 2000. DC link parameters: Ld = 2 mH; Cd = 3200 μF. Source impedance: Zs = j0.1884 Ω (=3%).